Application No.: 09/785,424

Art Unit: 2871

Docket No.: 8733.401.00-US

Page 2

forming a data line, a source electrode and a drain electrode on the active layer;

forming a passivation layer on the gate-insulating layer, the passivation layer covering the data line, source electrode and drain electrode;

dry-etching a surface of the passivation layer with a gas without using a photo mask such that the surface is embossed and has a plurality of random uneven portions; and

forming a reflective electrode on the embossed surface of the passivation layer such that an exterior surface of the reflective electrode is embossed.

11. (Twice Amended) A liquid crystal display device comprising:

upper and lower substrates with a liquid crystal layer interposed therebetween;

a gate line and a gate electrode on the lower substrate;

a gate-insulating layer on the lower substrate, the gate-insulating layer covering the gate line and gate electrode;

an active layer on the gate-insulating layer;

a source electrode and a drain electrode on the active layer;

a data line on the gate-insulating layer;

a passivation layer on the data line, source electrode, and drain electrode, an entire surface of the passivation layer being embossed and having a plurality of random uneven portions; and

an embossed reflective electrode on the passivation layer.